

Amendments to the Specification:

Please replace the paragraph that begins on page 6, line 4, with the following paragraph:

The CPU 29 is the central control mechanism and executes instructions code stored in memory, for example ROM 37, to perform certain functions of the system. For example, the CPU 29 processes certain data to control the generation of the folders and resultant program list in accordance with the teachings of the present invention. In addition, the CPU 29 receives and processes the user input, received from the front panel buttons or switches 40 and the photo detector circuit 39 to provide the user functionality and access to the system described herein. ~~In addition, the CPU 29 accesses user settings/preferences for processing of information and configuration of the system. The user settings are stored in the non-volatile memory, such as electrically erasable programmable read-only memory (EEPROM) 38. In addition, the CPU 29 accesses user settings/preferences for processing of information and configuration of the system. The user settings are stored in the non-volatile memory, such as electrically erasable programmable read-only memory (EEPROM) 38.~~ In addition, the CPU 29 maintains a list of pointers, stored in static random access memory (SRAM) 36, to the channel information and program information stored in the SRAM 51. Thus, when a user wishes to display a form of the EPG on the screen, the CPU 29, accessing pointers stored in the SRAM 36, communicates to the transport IC ~~[[34]]~~ 34 to retrieve the data from the data buffer (SRAM) 51 identified by the pointers. The CPU then formulates the format and other digital data which forms the guide or list on the screen and forwards the data representative of the guide/list to the transport IC ~~[[34]]~~ 34 which forwards the data to the DRAM 25a of the MPEG video decoder 25 for subsequent output to the screen. The CPU 29 also accesses to communication networks through telephone line or other cable connections using a communication interface 34.